OF 1

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF PATENT AND TRADEN		ATTY DOCKET NO. 205129US2RD		New Application A, et al.		
LIST OF REFERENCES CITED BY APPLICANT			APPLICANT Tomohisa KIMURA, et al.			1.s.		
•				FILING DATE Herewith		GROUP		040 09/8
		•		U.S. PATENT DOCUMENTS				11
EŻAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING D. IF APPROP	
	AA							
	AB				`			
	AC							
-	AD							
ļ	AE			_			<u> </u>	
	AF						<u></u>	
	AG		<u></u>					
ļ	АН		<u></u>			<u> </u>		
ļ'	Al					ļ		
	AJ	<u> </u>				 		
	AK	<u> </u>				 		
	AL	<u> </u>		<u> </u>				
·	AM					├		
	AN		<u></u>			<u>i</u>		
			FO	REIGN PATENT DOCUMENTS				
		DOCUMENT NUMBER	DATE	COUNTRY		YES	TRANSLATION	NO
M.S.	AO	10-261004	09/29/98	Japan (with English Abstract)				x
	AP		I					
	AQ					<u> </u>		
	AR							
	AS		l					
	AT		i					
	AU							
	AV	<u> </u>	<u></u>			<u> </u>		
		OTHER REF	ERENCES (Including Author, Title, Date, Pe	ertinent Pages, e	itc.)		
M.S.	AW	Kevin J. KERNS, et al., "STABLE AND EFFICIENT REDUCTION OF SUBSTRATE MODEL NETWORKS USING CONGRUENCE TRANSFORMS," IEEE/ACM International Conference on Computer Aided Design Digest of Technical Papers, 1995, pgs. 207-214						
Mr5.	AX	Eli CHIPROUT, et al., "ASYMPTOTIC WAVEFORM EVALUATION AND MOMENT MATCHING FOR INTERCONNECT ANALYSIS," Kluwer Academic Publishers, Chapters 1, 2, and 4, 1994, pgs. 1-38, 67-84						
MS.	AY	Balsha R. STANISIC, et al., "ADDRESSING SUBSTRATE COUPLING IN MIXED-MODE IC'S: SIMULATION AND POWER DISTRIBUTION SYNTHESIS," IEEE Journal of Solid-State Circuits, Vol. 29, No. 3, March 1994, pgs. 226-238						
	AZ							
Examiner	N	noma 1	Thru	l.M	Date Cor	nsidered	11/29/	104
				t citation is in conformance with Min with next communication to appli		ne through	citation if not in	